REMARKS

Claims 1-4, 9-14, 16-22, 27-30, 32, 34-37, 42-46, 51-54, and 56 stand rejected under 35 U.S.C. § 103(a) as being obvious over United States Patent Nos. 6,256,235 (Lee), 6,266,252 (Karabastos), 5,955,889 (Taguchi), 6,067,594 (Perino), and 6,510,100 (Grundon). Claims 5-8, 15, 23-26, 31, 33, 38-41, 47-50, 55, and 57 were objected to, but deemed to be allowable. Claims 59-61 were listed by the Office Action in the rejection, but had been withdrawn from consideration.

Claims 1, 2, 9, 10, 18, 19, 20, 27, 28, 36, 43, 44, 51, 52, and 59-61 were cancelled. Allowable claims 8, 15, 26, 31, 33, 41, 50, 55, and 57 were rewritten in independent form including all features of the parent and any intervening claims.

Claims 3, 21, 37, and 45, were rewritten in independent form. Accordingly, these amendments did not raise new issues. Claims 3, 21, 37, and 45 include the general features of a memory module including termination circuitry for lines on the module and a termination voltage generator for generating a termination voltage signal for use by the termination circuitry.

The Office Action cites Grundon as teaching a termination voltage generator on the module. However, Grundon, merely shows connecting V_{cc} to the termination circuitry. In accordance with industry usage, V_{cc} is the chip supply voltage supplied to the module by the motherboard to which it is mounted. Grundon merely shows a node that is connected to V_{cc} , not a termination voltage generator for generating a termination voltage signal. None of the cited art shows a memory module including a termination voltage generator for generating a termination voltage signal. Accordingly, claims 3, 21, 37, 45, and all claims depending therefrom, are allowable. Applicants respectfully request the rejection of these claims be withdrawn.

The Examiner is invited to contact the undersigned attorney at (713) 934-4060 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

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